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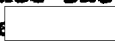
Chief, Research & Development Branch

20 October 1961



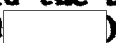
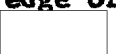
Chief, Research & Development Laboratory

 Sketches

50X1

1. The attached sketches show what we believe would be the final form of the  if it is produced by us. While not shown, the dimensions are roughly 3 1/2 x 5 1/2 x 1 1/4 inches. I make mention of this fact because this drawing creates the illusion, for me at least, that it is somewhat bigger than that.


50X1

2. On this particular model the battery doors which would hold the battery to power the  (similar to those on the  and ) would be placed on the back edge of the keyer as opposed to the bottom as they are on the .

50X1

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3. The drawing of the separate power supply is included just to give you some idea what they would look like if we were to take this course. It would have a built-in charger. As usual, our fellows are optimistic and believe that possibly we could scrunch down the size on the  and pick up at least 1/2 to 3/4 inch in length and a similar amount (by comparison) in the width. Hopefully we can do that but think it would be better if that come as a surprise rather than a requirement.

50X1



Acting

50X1


Attachments:





CK-15 Sketches

CTB

Lab/CTB/rkb (20 October 1961)

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DATE:  1980 REVIEWER: 

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Chief, R&D Branch

4 January 1962

THRU : Acting Chief, R&D Laboratory

50X1

Proposal for Aperture Plate Memory for

50X1

1. The uses a tape memory with a three-level output and the memory in the has a three-level output. Therefore, the aperture plate memory of the could be used in the . The aperture plate memory would be smaller, have no moving parts, use less power, and possibly cost less than the present tape system.

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2. A brief look at the circuit diagram for the RS-18 shows that the tape memory and exciter-modulator could be replaced by circuits like those of the . For a storage capacity of 165 groups, 36 aperture plates would be required.

50X1

3. If it becomes desirable to replace the tape system of the the flux limited memory system of the would be a possible solution.

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50X1

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